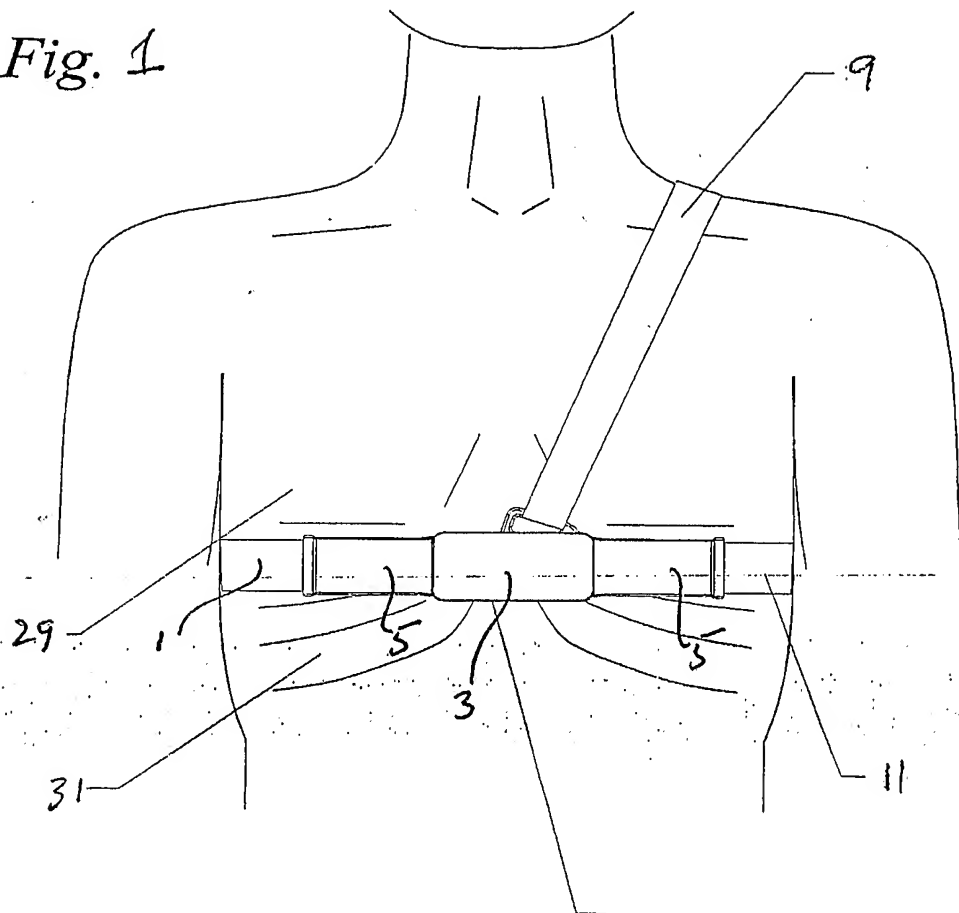
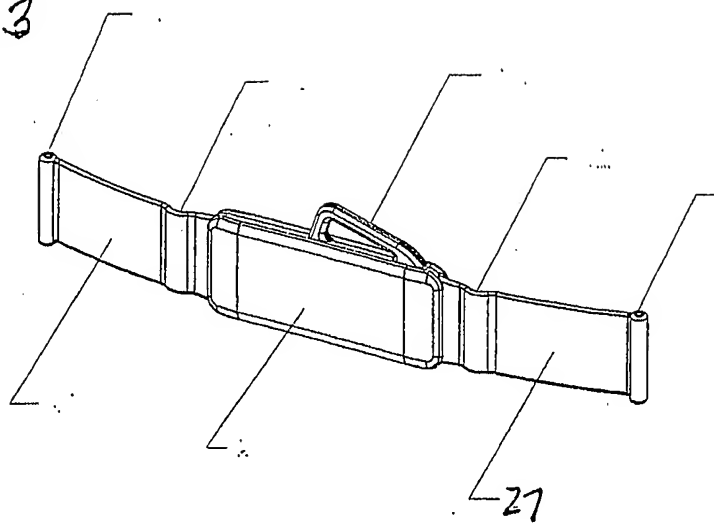
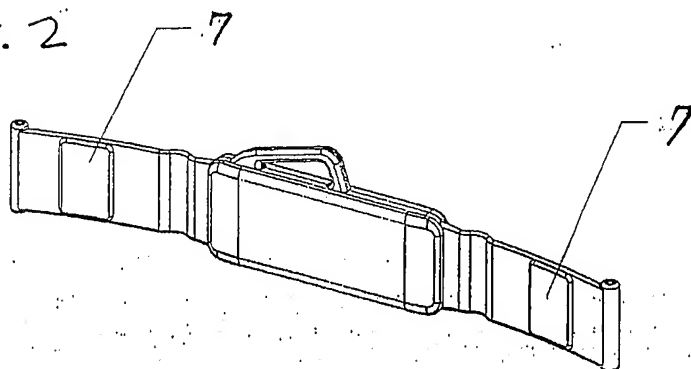


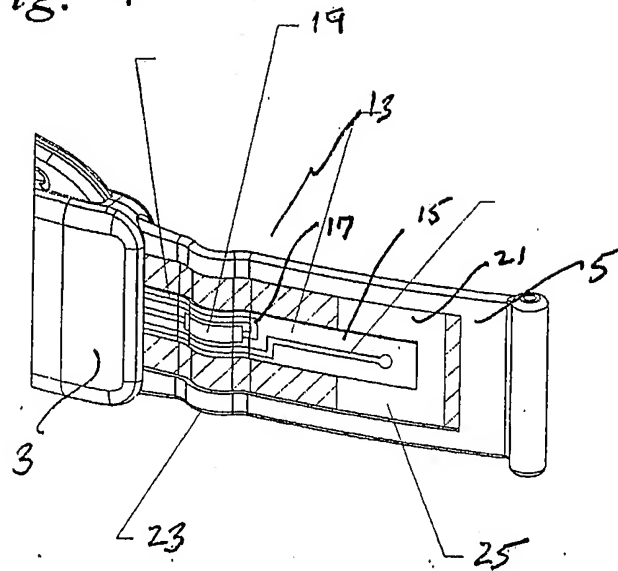
*Fig. 1*

*Fig. 3*

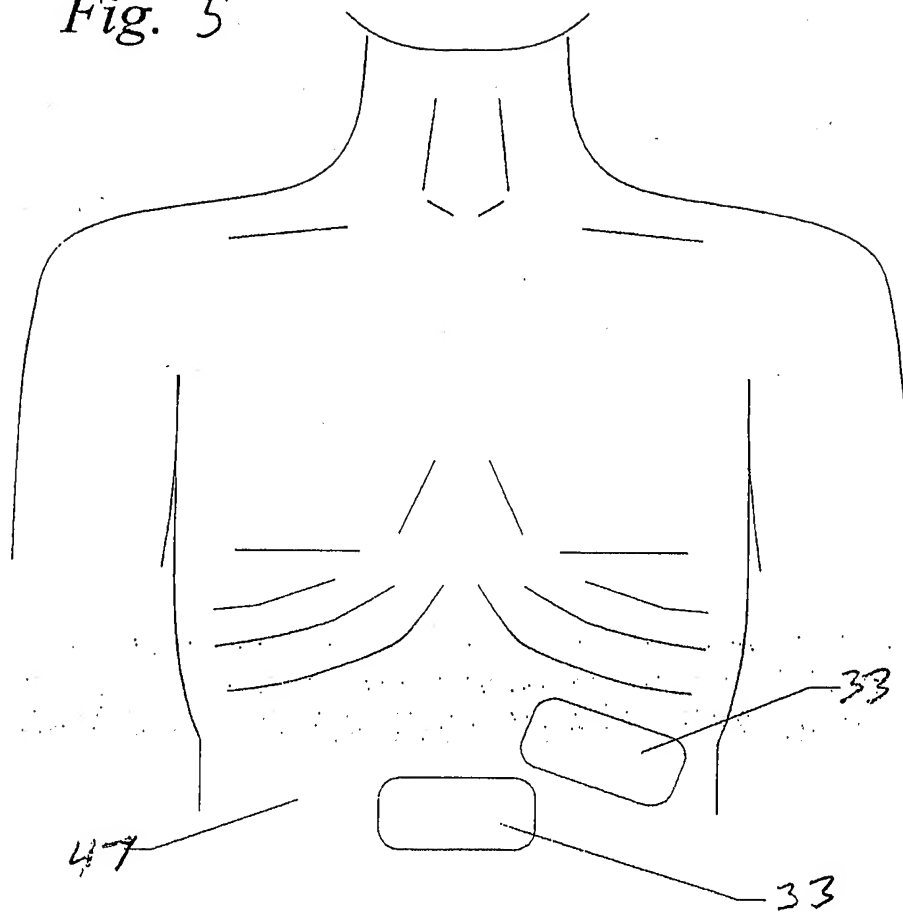


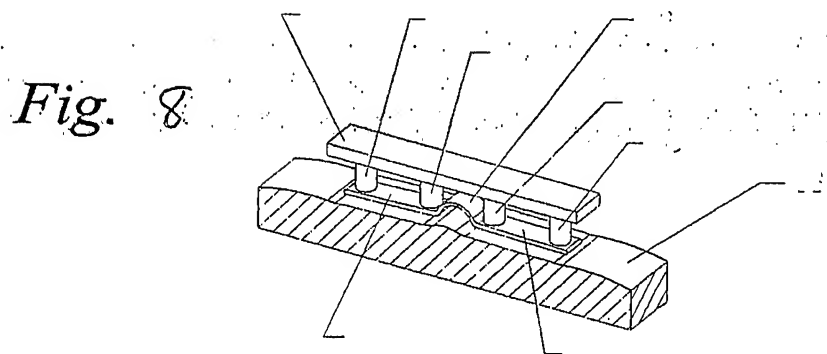
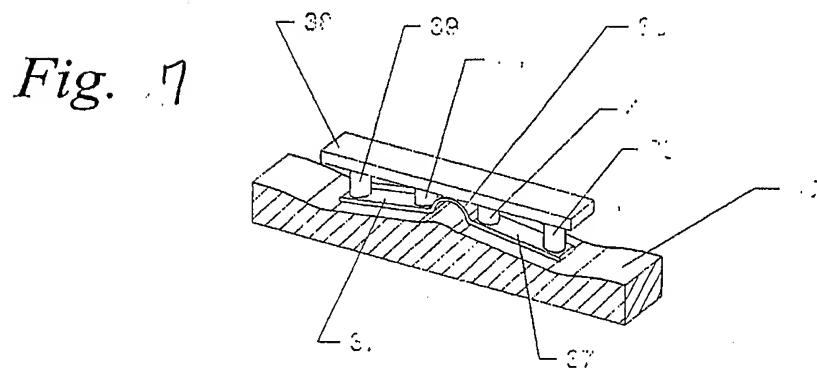
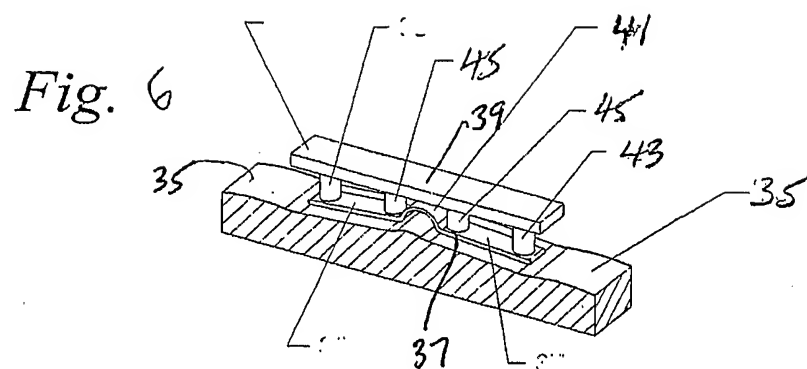
*Fig. 2*



*Fig. 4*

*Fig. 5*





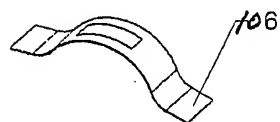
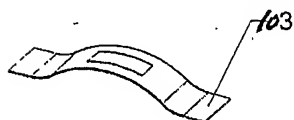
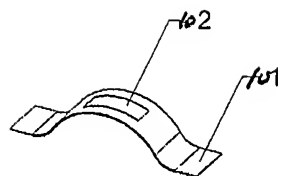
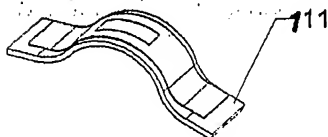
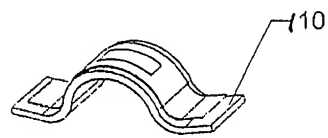
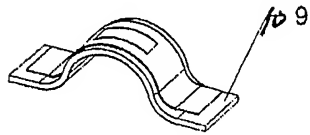
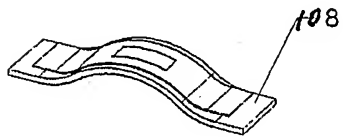
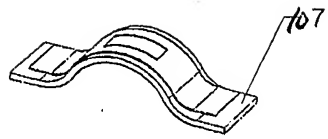
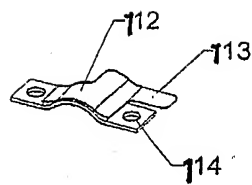
*Fig. 9*

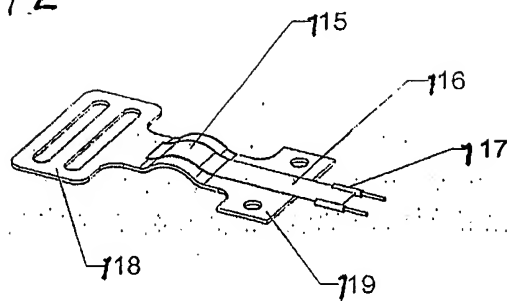
Fig. 10



*Fig. 11*



*Fig. 12*





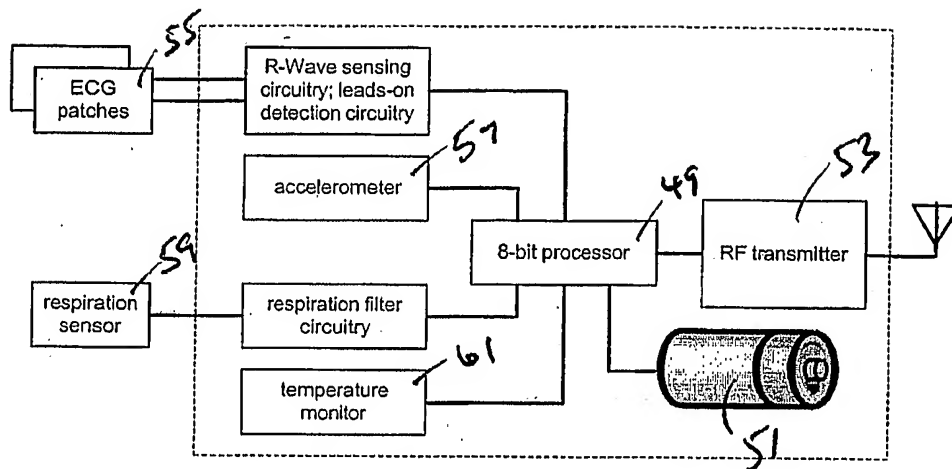


Fig. 13

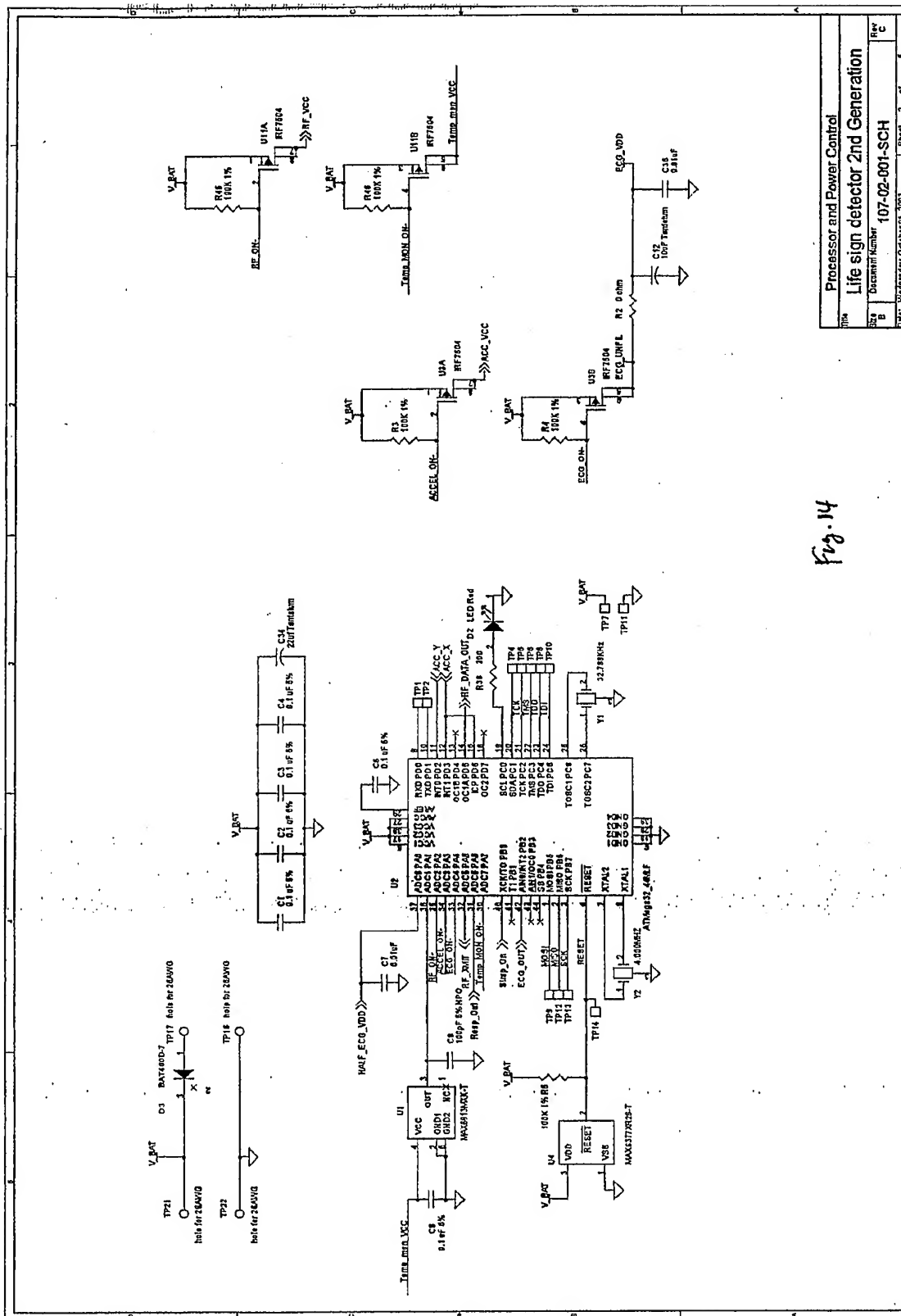


Fig. 14

Processor and Power Control	
Life sign detector 2nd Generation	
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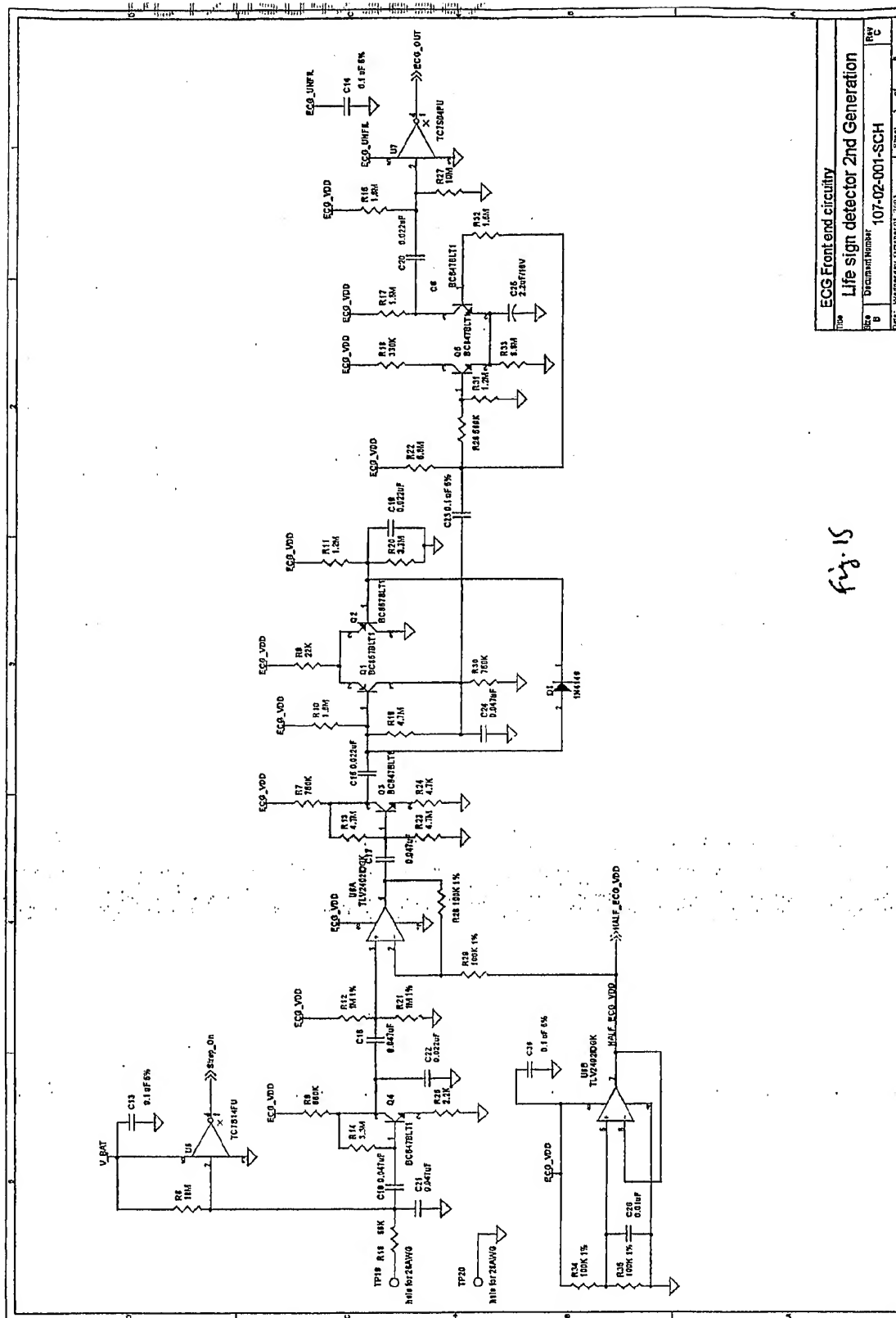
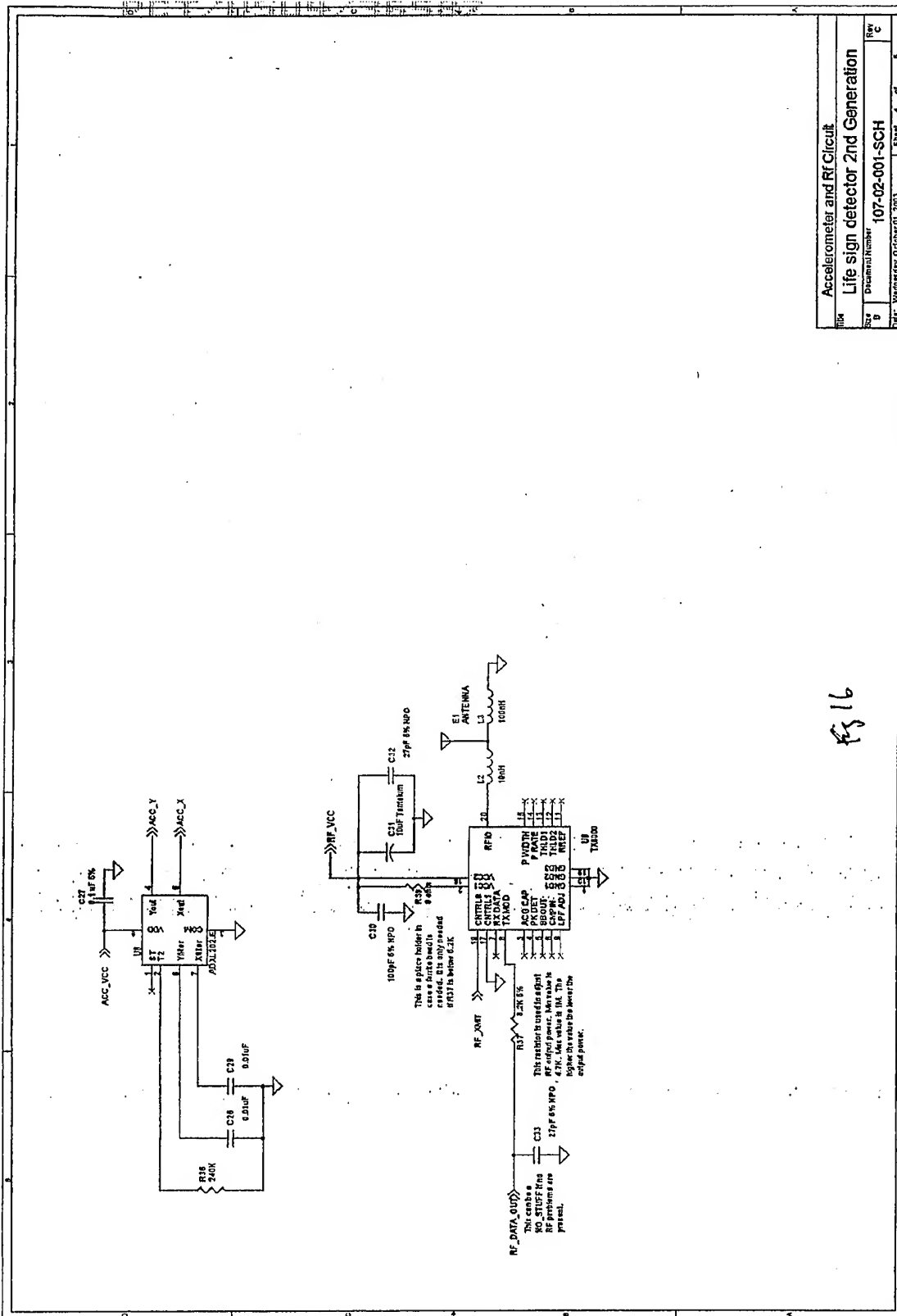
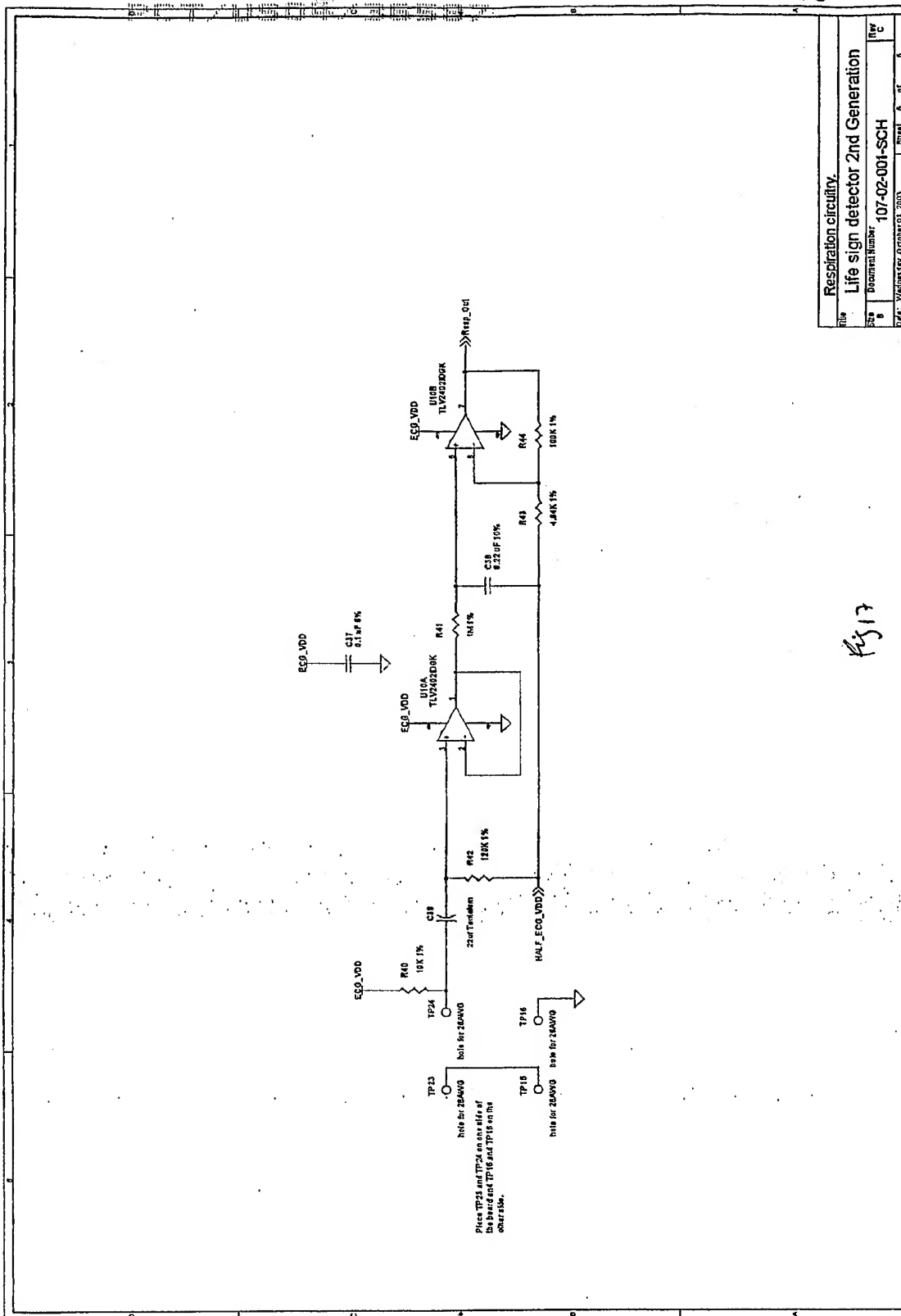


Fig. 15

ECG Front end circuitry			
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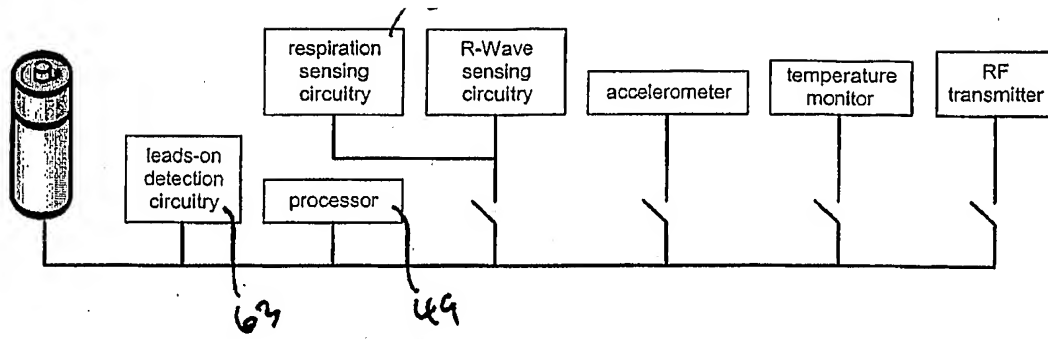


Fig. 18

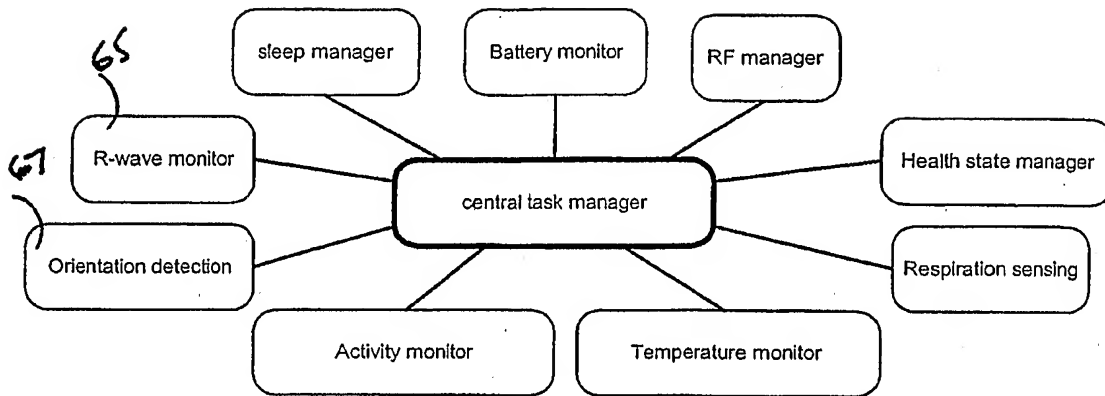


Fig. 19

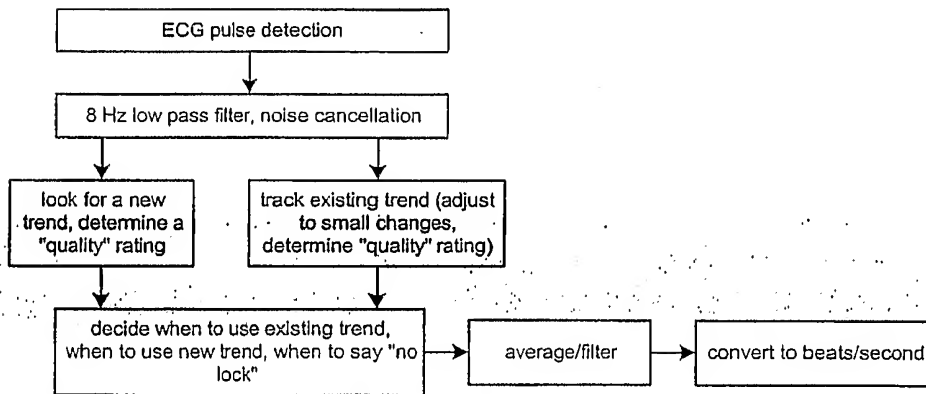


Fig. 20

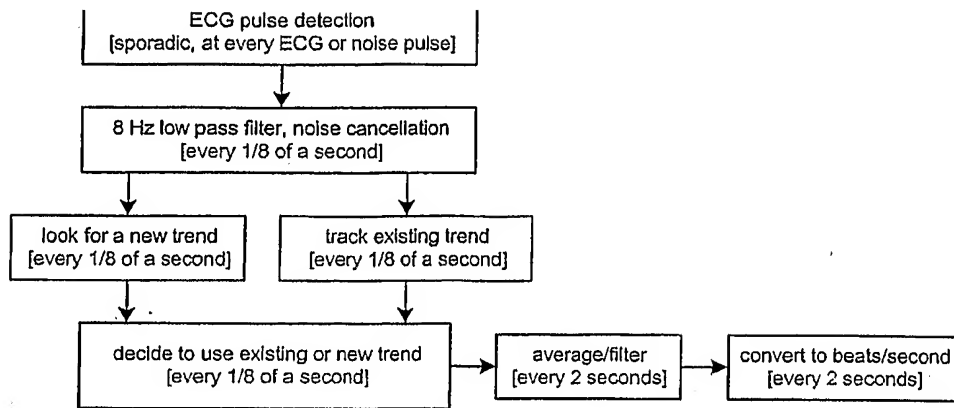


Fig. 21

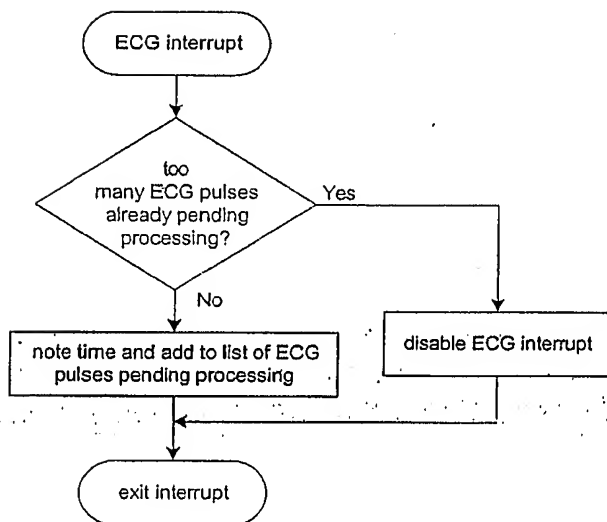


Fig. 22



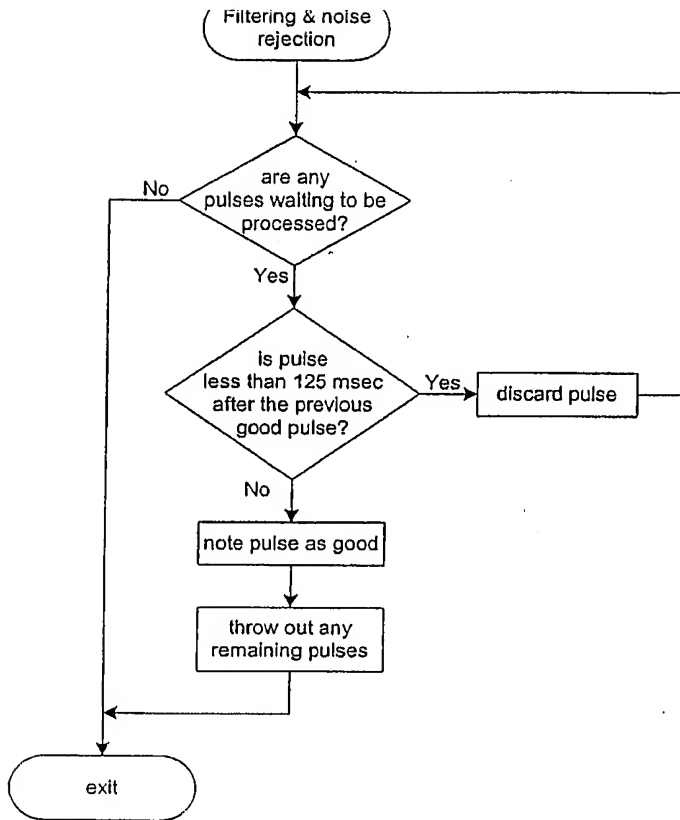


Fig. 23

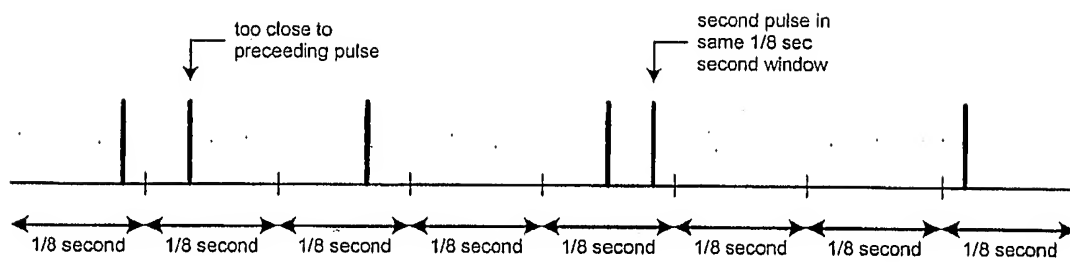


Fig. 24

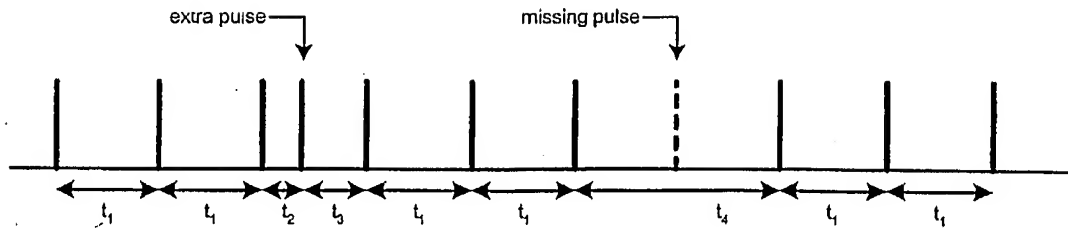


Fig. 25

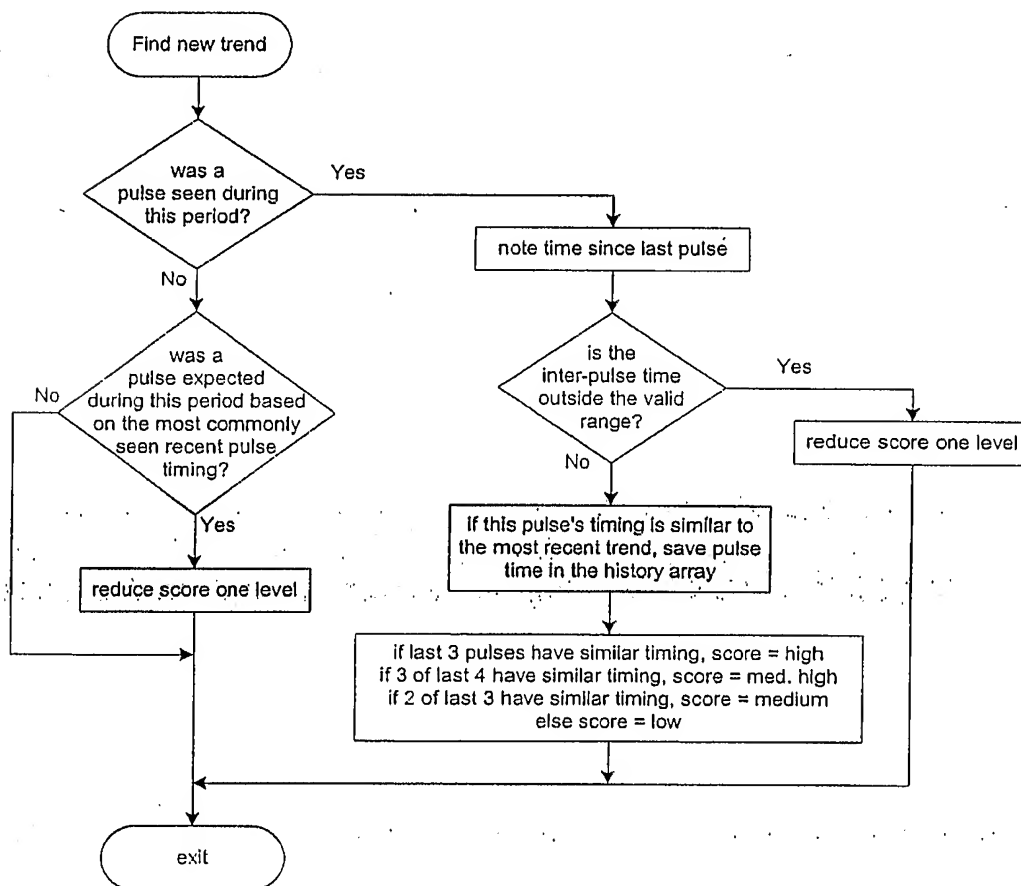


Fig. 26

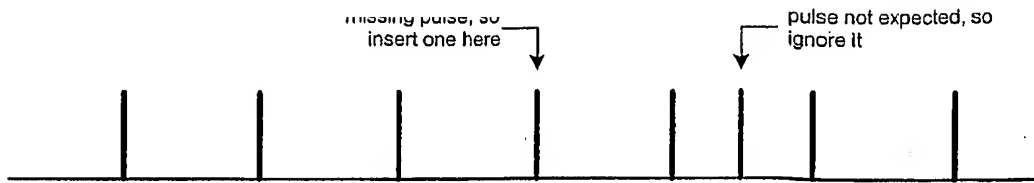


Fig. 27

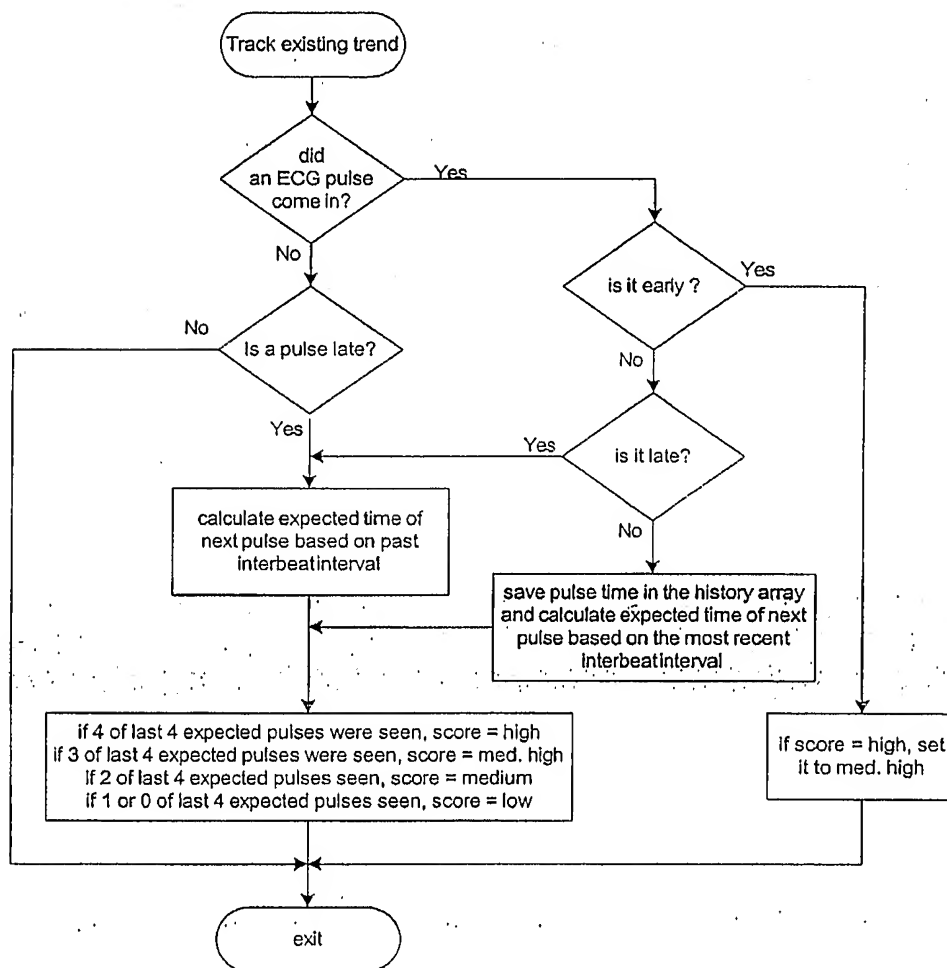


Fig. 28

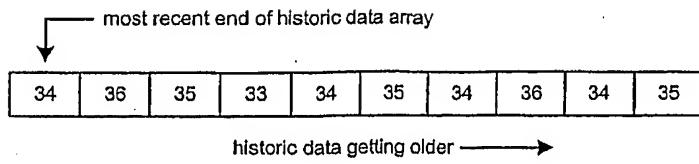


Fig. 29

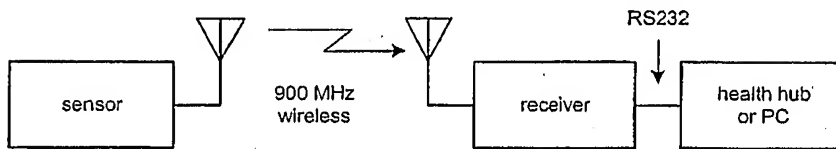


Fig. 30

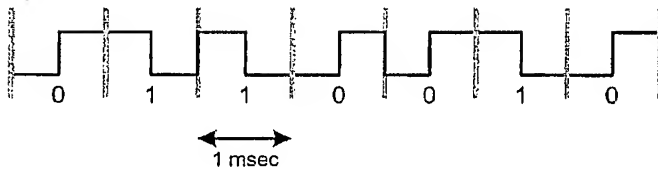


Fig. 31



Fig. 32

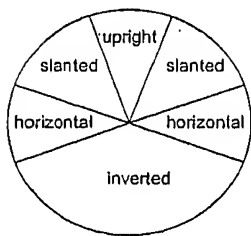


Fig. 33